Fig. 1



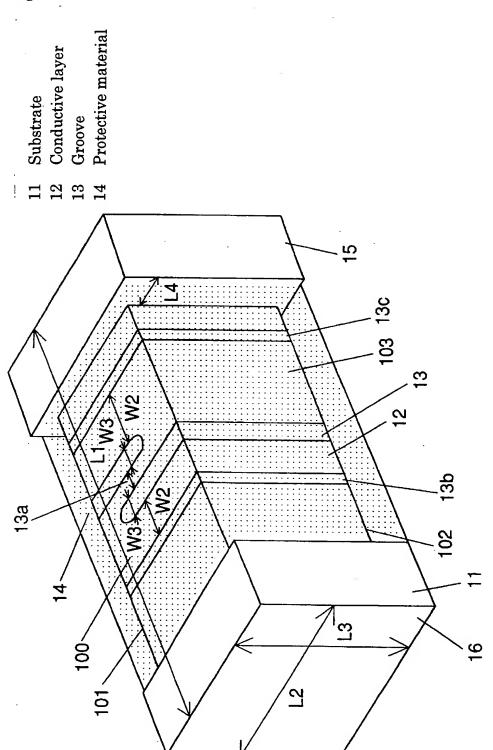




Fig. 2

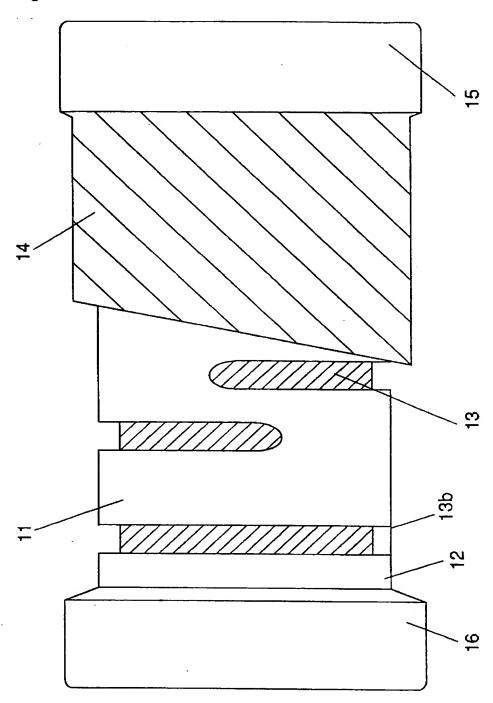




Fig. 3

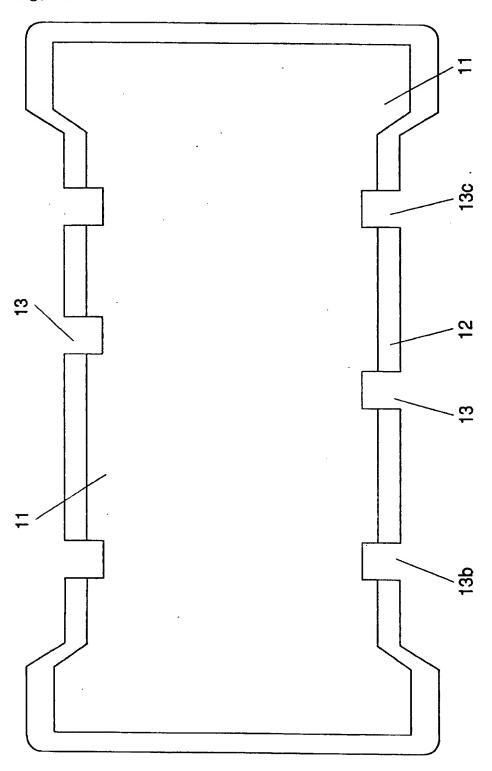




Fig. 4

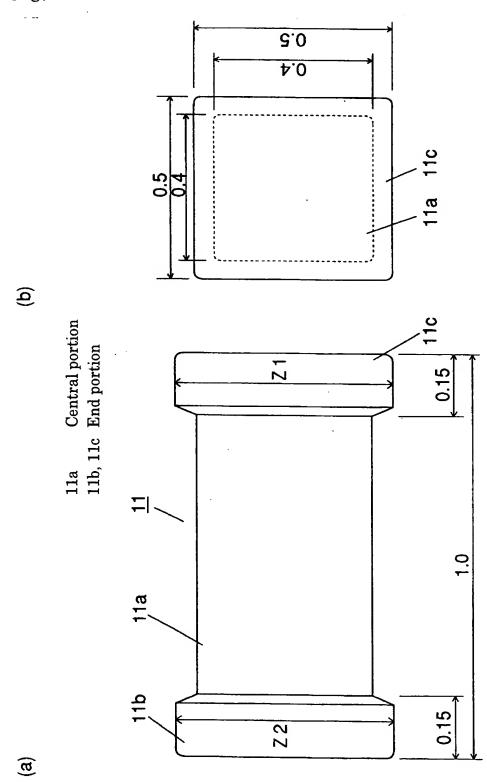




Fig. 5

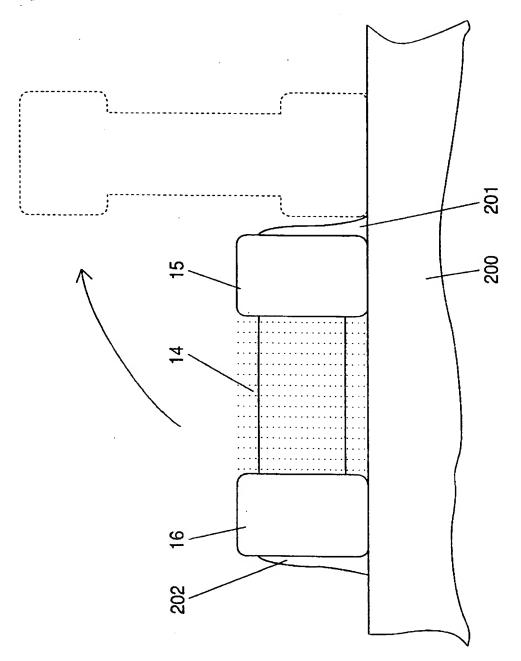
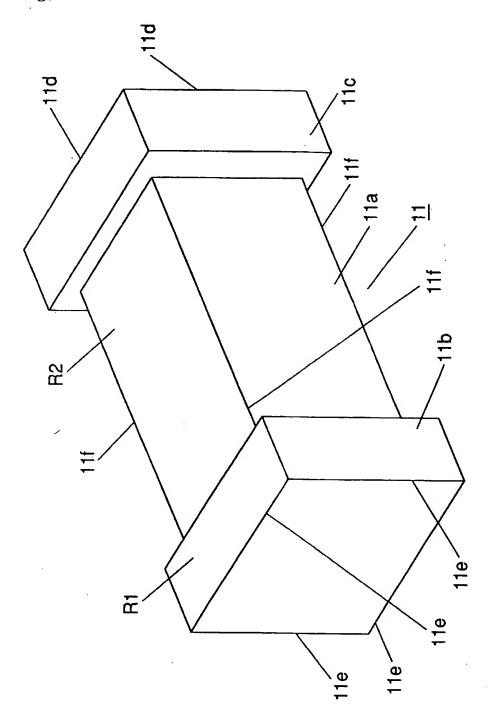


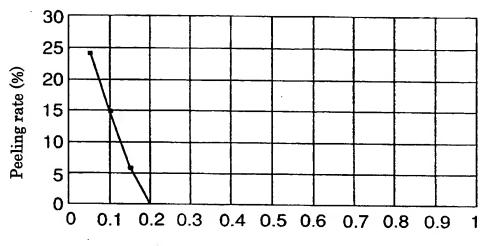


Fig. 6









Surface roughness (µm)

Fig. 8

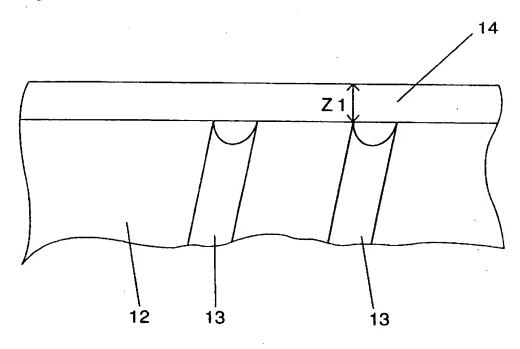




Fig. 9

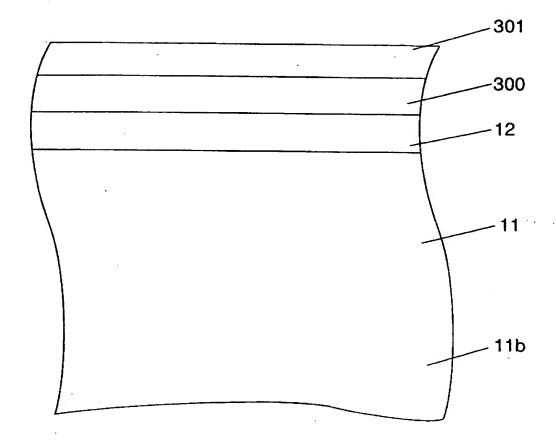




Fig. 10

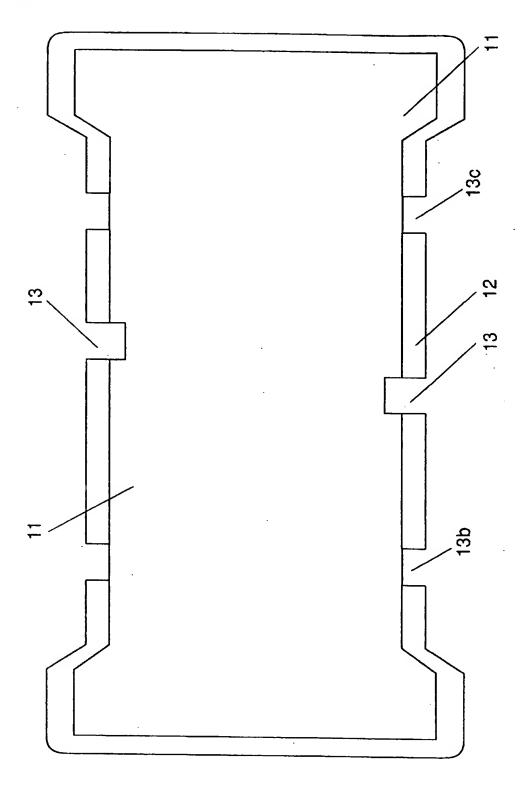
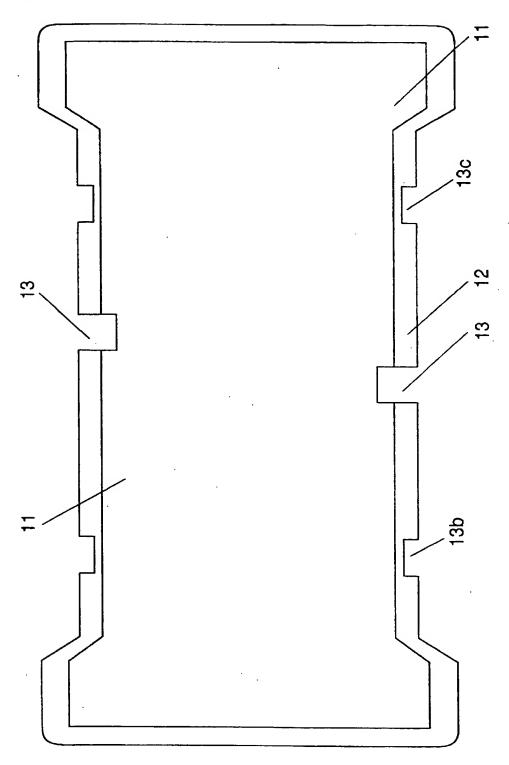


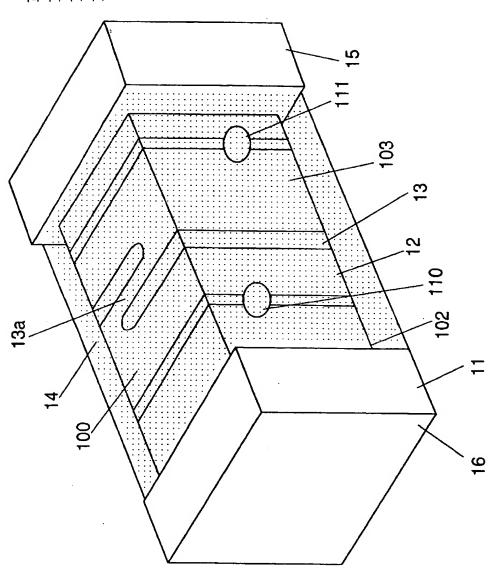


Fig. 11





Substrate Conductive layer
Groove
Protective material







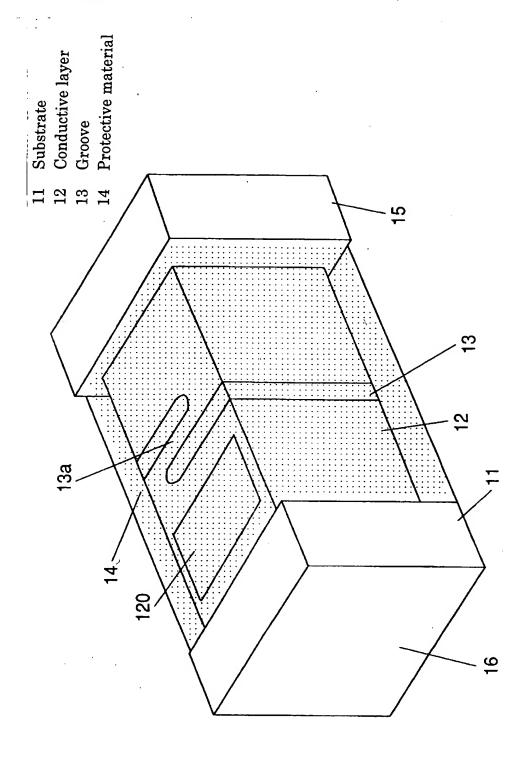




Fig. 14

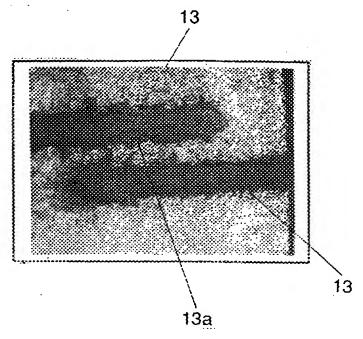


Fig. 15

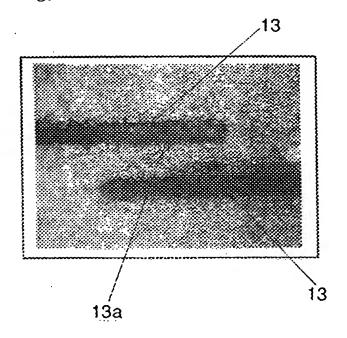
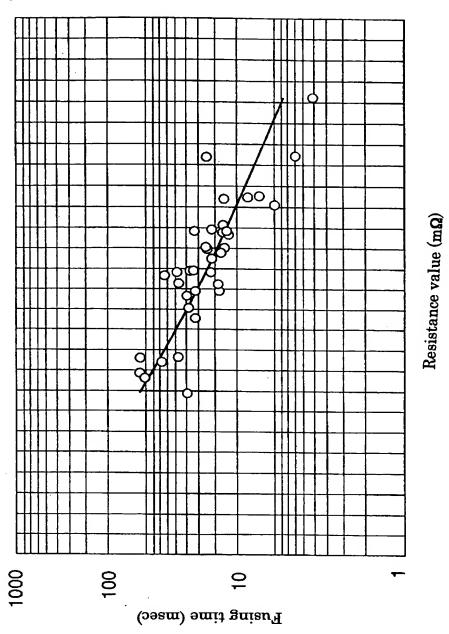




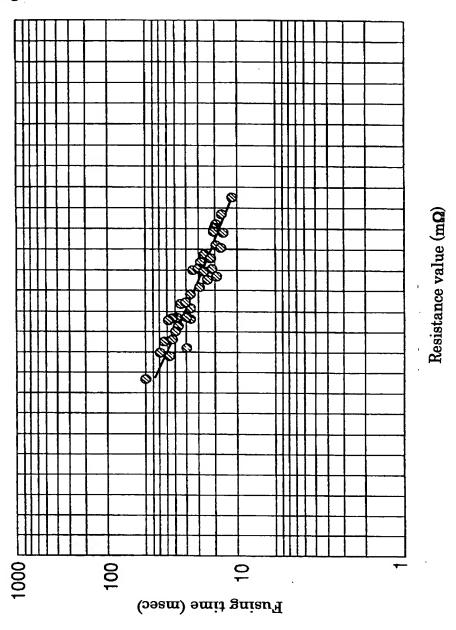
Fig. 16



51



Fig. 17



52



Fig. 18

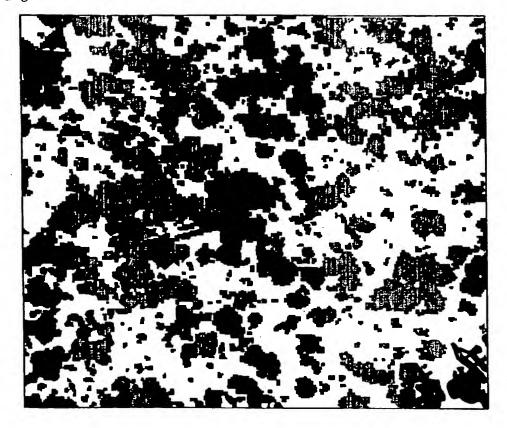




Fig. 19

